



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of)
)
Akiharu MIYANAGA et al.)
)
Serial No.: 09/241,695)
)
Filed: February 2, 1999)
)
For: Semiconductor Device And)
Process For Producing The Same)
)
Art Unit: 2811)
)
Examiner: S. Hu)

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Signature Donna Janus Moss
Date 6/12/2001

Commissioner for Patents
Washington, D.C. 20231

AMENDMENT

Sir:

Please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend claims 1, 29 and 42. Claims 1, 3, 15, 18, 21, 24, 27-34, 42-49 are to read as follows:

1. (Amended) A semiconductor device comprising a plurality of MOSFETs formed in a single semiconductor substrate,

each of the plurality of MOSFETs comprising:

a source region and a drain region each including a first impurity;

a channel forming region being formed between the source region and the drain

region; and

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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application Number	09/241,695
	Filing Date	February 2, 1999
	First Named Inventor	Akiharu Miyanaga
	Group Art Unit	2811
	Examiner Name	S. Hu
Total Number of Pages in This Submission	Attorney Docket Number	SEL 123

ENCLOSURES (check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Cook, Alex, McFarron, Manzo, Cummings & Mehler, Ltd. Stephen B. Heller
Signature	<i>Stephen B. Heller</i>
Date	June 12, 2001

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